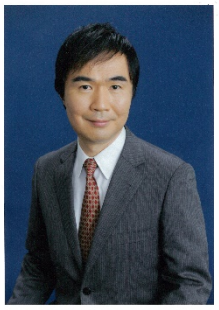
氏名	 松岡　聡（まつおか　さとし）
所属機関・部署・役職	理化学研究所・計算科学研究センター・センター長 東京工業大学　数理・計算科学系・特任教授
学歴 （大学卒業以降）	1986 年　東京大学理学部情報科学科卒業 1988 年　東京大学大学院理学系研究科情報科学専攻修士課程修了 1989 年　東京大学大学院理学系研究科情報科学専攻博士課程在籍中，助手に採用 1993 年　博士（理学）（東京大学）取得
研究歴 （主な職歴と 研究内容） 個人受賞歴 （抜粋） 国際会議 委員長職等	1989～1993 年　東京大学理学系研究科情報科学専攻・助手 オブジェクト指向・並列プログラミング言語処理系に関する研究 1993～1996 年　東京大学工学系研究科情報工学専攻・講師 オブジェクト指向・並列言語処理系・UI に関する研究 1996～2001 年　東京工業大学情報理工学研究科数理・計算科学専攻・助教授 クラスタ・グリッド計算，高速 JIT コンパイラなどに関する研究 2001 年～2018 年 3 月　東京工業大学学術国際情報センター・教授 2002 年～　国立情報学研究所客員教授（併任） クラスタ・グリッド計算，省電力高性能計算，大規模データシステム、大規模耐故障性等に関する研究 2012 年～2018 年　理化学研究所・計算科学センター(神戸・京コンピュータ) 客員研究員、ポストペタフロップスのスーパーコンピュータに関する研究 2013 年～ビッグデータ/AIと HPC の融合に関する研究 2014 年～ポストムーア・スーパーコンピューティングに関する研究 2016 年～2018 年 3 月　産業技術総合研究所　人工知能研究センター・特定フェロー　AI 橋渡しクラウド（ABCI)の技術リーダー 2017 年 2 月～2018 年 3 月産業技術総合研究所　実世界ビッグデータ・オープンイノベーションラボ・センター長　HPC とビッグデータ・AIの融合の研究 2018 年 4 月～理化学研究所・計算科学研究センター・センター長 （東京工業大学　数理・計算科学系・特任教授　兼職）

	主な受賞等 1999 年 11 月 1 日 （社）情報処理学会 坂井記念賞 2005 年 4 月 1 日 日本学術振興会賞、（独）日本学術振興会 2008 年 6 月 17 日 ISC’08 Award, International Supercomputing Conference （欧州国際スーパーコンピュータ会議賞） 2009 年 11 月 6 日 ISC Fellow, International Supercomputing Conference （欧州国際スーパーコンピュータ会議フェロー） 2010 年 1 月 11 日 People to Watch in 2010, The HPCWire （HPC ワイア誌・2010 年の注目人物） 2011 年 11 月 8 日 ACM Fellow, Association for Computing Machinery （米国計算機学会 ACM・フェロー、日本人では史上 7 人目） 2011 年 11 月 17 日 ACM Gordon Bell Prize （米国計算機学会 ACM ゴードンベル賞） 2011 年 11 月 22 日 電気科学技術奨励賞、「運用世界一グリーンスパコンの TSUBAME2.0 を実現した研究開発」 2012 年 4 月 12 日 文部科学大臣表彰 科学技術賞（開発部門）、「運用世界一グリーンペタスパコンの開発」 2014 年 3 月 5 日 大川出版賞・岩波講座 計算科学 別巻「スーパーコンピュータ」 （ノーベル賞受賞の赤崎氏と同時受賞） 2014 年 11 月 18 日 IEEE Computer Society Sidney Fernbach Memorial Award (HPC・スーパーコンピュータ分野で最高峰の賞・日本人初) 2015 年 11 月 17 日 HPC Wire 2015 Readers Choice Awards Outstanding Leadership in HPC (co-award with Prof. Jack Dongarra, Univ. Tennessee) 2017 年 1 月 11 日 People to Watch in 2017, The HPCWire （HPC ワイア誌・2017 年の注目人物） 2018 年 6 月 11 日 2018 ACM HPDC Achievement Award 2019 年 3 月 12 日 SCAsia 2019 Asia HPC Leadership Award 国際会議プログラム委員長・大会委員長歴 1996 年 JSSST International Symposium on Objects Technologies for Advanced Software (ISOTAS’96) プログラム委員長 (Program Chair) 1997 年 European Conference on Object-Oriented Computing (ECOOP’97) プログラム委員長 (Program Chair) 1999 年 ACM Object-Oriented Programming: Languages, Systems and Applications (OOPSLA’2000) Doctoral Symposium 委員長 1999 年 International Symposium on Computing with Objects for Parallel Environments

	(ISCOPE'99) プログラム委員長 (Program Chair) 2001 年 AITO/JSSST Reflection '2001 大会委員長 (General Chair) 2002 年 ACM Object-Oriented Programming: Languages, Systems and Applications (OOPSLA'2002) プログラム委員長 (Program Chair) 2003 年 IEEE Computing Clusters and the Grid (CCGrid'2003) プログラム委員長 (Program Chair) 2004 年 HPC Asia 2004 プログラム副委員長 2004 年 IEEE/ACM Supercomputing Technical Track – "Networking" Area Chair 2005 年 IEEE Cluster Computing Vice Program Chair 2005 年 IEEE HiPC Vice Chair 2006 年 IEEE Computing Clusters and the Grid (CCGrid'2006) 大会委員長 (General Chair) 2007 年 IEEE Computing Clusters and the Grid (CCGrid'2007) Program Vice Chair 2008 年 IEEE IPDPS- HPPAC (High-Performance Power Aware Computing) Program Co-Chair 2008 年 IEEE Computing Clusters and the Grid (CCGrid'2008) Program Vice Chair 2008 年 IEEE/ACM Supercomputing Technical Track – "Grids" Area Chair 1999-2006 年 The Global Grid Forum Steering Group --- Area Chair 2009 年 ACM/IEEE Supercomputing: Technical Papers Chair 2008-2011 年 ACM/IEEE Supercomputing - Steering Group Member 2011 年 ACM/IEEE Supercomputing: Communities Chair 2011 年 ACM HiPC Program Chair 2012 年 ACM SIGHPC Advisory Board Member 2013 年 International Parallel Processing Conference – Vice Chair 2013 年 ACM/IEEE Supercomputing: Technical Program Chair 2014 年 ACM/IEEE Supercomputing: Emerging Technologies Chair 2015 年 IEEE CCGrid Conference Papers – Architectures Track Chair 2015 年 IEEE Cluster Conference – Program Chair 2015 年 ACM/IEEE Supercomputing (2015) – Technical Program, Gordon Bell Committee Liaison 2016 年 International Supercomputing Conference (ISC16) – Program Chair 2016 年 IEEE Cluster Computing Conference (Cluster 2016) – General Co-Chair 2016 年 ACM/IEEE Supercomputing: Awards Chair 2016 年~ 米 DoE Advanced Scientific Computing Advisory Committee (ASCAC) member 2017 年 International Supercomputing Conference (ISC17) – HPC in Asia Chair

	2017 年 International Parallel Computing Conference (ICPP17) – Architecture Track Chair 2018 年 SIAM Parallel Programming Conference Co-Chair 2018 年 Supercomputing Asia Deputy Conference Chair 2018 年 ACM Gordon Bell Prize – Selection Committee Chair
スーパーコンピュータ TSUBAME 等ランキング及び受賞	(下記、Top500, Green500, Graph500, Green Graph500 はランキング発表は年二回行われ、6 月あるいは 7 月が上半期、11 月が下半期のランキング) **TSUBAME1** 2006 年 6 月 スパコン計算性能ランキング Top500 ランキング世界 7 位 2006 年 6 月、11 月、2007 年 6 月 アジア 1 位 (3 回連続) 2006 年 6 月、11 月、2007 年 6 月、11 月 日本 1 位 (4 回連続) **TSUBAME2** 2010 年 11 月 Top500 世界 4 位(日本 1 位)/2011 年 6 月、11 月世界 5 位(日本 2 位) 2010 年 11 月(下半期)、2011 年 6 月スパコン省エネランキング Green500 世界 2 位、運用スパコンとしては世界 1 位 2011 年 11 月 ビッグデータランキング Graph500 世界 3 位/2012 年 6 月世界 4 位 2011 年 11 月 16 日 HPCwire Annual Awards (Reader's & Editors Choice 3 選) 2012 年 5 月 17 日 NVIDIA CCOE Achievement Award – Tokyo Tech **TSUBAME-KFC** 2013 年 11 月 17 日 The Green Graph 500 世界一位 2013 年 11 月ランキング 2013 年 11 月 18 日 The Green 500 世界一位 2013 年 11 月ランキング 2014 年 6 月 The Green 500 世界一位 2014 年 6 月ランキング 2015 年 11 月 The Green 500 世界二位 2015 年 11 月ランキング **TSUBAME3** 2017 年 6 月 The Green 500 世界一位 2017 年 6 月ランキング **京コンピュータ** 2014 年 6 月 The Graph 500 世界一位 2014 年 6 月ランキング 2015 年 7 月 The Graph 500 世界一位 2015 年 7 月ランキング~2017 年 11 月ランキング(6 回連続)

著書

[1] 岩波講座　計算科学　別巻「スーパーコンピュータ」小柳 義夫/中村 宏/佐藤 三久/松岡 聡【著】岩波書店, 2013 (大川出版賞受賞)

[2] "Petascale Computing Algorithms and Applications" --- Chapter 14 The Road to TSUBAME and Beyond"Chapman & Hall Crc Computational Science Series, pp.289-310

[3] "The Grid 2: Blueprint for a New Computing Infrastructure" ---Chapter 24 Application-Level Tools Morgan-Kauffman, San Francisco, pp. 463-490, 2004

[4] 岩波講座 インターネット〈4〉ネットワークアプリケーション 岩波講座 インターネット (単行本) 砂原 秀樹, 中田 秀基, 後藤 滋樹, 知念 賢一, 松岡 聡 岩波書店 2003

[5] "Java プログラミング例題集" 大野, 前田, 井田, 松岡 聡, 中田 秀基　編 共立出版 1997

[6] "オブジェクト指向計算, 共編: 所, 垂水, 松岡　聡, 岩波書店, 1994

Citation Information (Google Scholar Citations, Properly Sanitized, as of April 2018)

Citations: 11671 total, (3816 since 2013)
H-Index: 52
H10-Index 163

(Please note that in Computer Science and some Applied Math, both journals and international conferences are rigorously refereed with full papers. For international conference it is typical to have 4 reviewers per paper, and for top conferences multiple review phases, with acceptance rates being 10-25%, and published through publishers such as ACM, IEEE, Springer, etc.)

Selected Publications (Since 1989, full list approximately 500 in total)

[1] Adrian Perez Dieguez, Margarita Amor, Ramon Doallo, Akira Nukada, Satoshi Matsuoka "Efficient Solving of Scan Primitive on Multi-GPU Systems", IPDPS2018, Vancouver, Canada, May 2018, 査読有(to appear)

[2] James Lin, Minhua Wen, Delong Meng, Xin Liu, Akira Nukada and Satoshi Matsuoka "Optimizations of Preconditioned Conjugate Gradient on TaihuLight for OpenFOAM", CCGrid 2018, Washington, DC, May 2018, 査読有(to appear)

[3] Jian Guo, Akihiro Nomura, Ryan Barton, Haoyu Zhang and Satoshi Matsuoka "Machine Learning Predictions for Underestimation of Job Execution Time on HPC System", SC Asia, Sentosa, Singapore, Mar 2018, 査読有 (to appear)

[4] Kazuaki Matsumura, Mitsuhisa Sato, Taisuke Boku, Artur Podobas, Satoshi Matsuoka "MACC : An OpenACC Transpiler for Automatic Multi-GPU Use", SC Asia, Sentosa, Singapore, Mar 2018, 査読有(to appear)

[5] Hamid Reza Zohouri, Artur Podobas, Naoya Maruyama, Satoshi Matsuoka "Combined Spatial and Temporal Blocking for High-Performance Stencil Computation on FPGAs Using OpenCL", FPGA18, Monterey, CA, USA, Feb 2018, 査読有(to appear)

[6] Artur Podobas, Satoshi Matsuoka "Designing and Accelerating Spiking Neural Networks using OpenCL for FPGAs", The 2017 International Conference on Field-Programmable Technology (FPT 2017), Melbourne, Australia, Dec 2017, 査読有

[7] Ikuro Sato, Ryo Fujisaki, Yosuke Oyama, Akihiro Nomura, Satoshi Matsuoka "Asynchronous, Data-Parallel

Deep Convolutional Neural Network Training with Linear Prediction Model for Parameter Transition", ICONIP 2017, Guangzhou, China, Nov 2017, 査読有

[8] Shota Kuroda, Toshio Endo, Satoshi Matsuoka "Applying Temporal Blocking with a Directive-based Approach", The Fourth Workshop on the LLVM Compiler Infrastructure in HPC(LLVM-HPC), in conjunction with SC17, Denver, Nov 2017, 査読有

[9] Artur Podobas, Hamid Reza Zohouri, Naoya Maruyama, Satoshi Matsuoka "Evaluating High-Level Design Strategies on FPGAs for High-Performance Computing", FPL 2017, Ghent, Belgium, Sep 2017, 査読有

[10] Yusuke Nagasaka, Akira Nukada, Satoshi Matsuoka "High-performance and Memory-saving Sparse General Matrix-Matrix Multiplication for NVIDIA Pascal GPU", The 46th International Conference on Parallel Processing (ICPP-2017), Bristol, UK, Aug 2017, 査読有(採択率 28.4%)

[11] James Lin, Zhigeng Xu, Akira Nukada, Naoya Maruyama and Satoshi Matsuoka "Optimizations of Compute-bound Scientific Kernels on SW26010 Many-core Processor", The 46th International Conference on Parallel Processing (ICPP-2017), Bristol, UK, Aug 2017, 査読有

[12] Jian Guo, Kun Qian, Björn Schuller, Satoshi Matsuoka "GPU-based Training of Autoencoders for Bird Sound Data Processing", IEEE ICCE-TW 2017, Taipei, Taiwan, Jun 2017, 査読有, Best Paper Award

[13] Kevin Brown, Tianqi Xu, Keita Iwabuchi, Kento Sato, Adam Moody, Kathryn Mohror, Nikhil Jain, Abhinav Bhatele, Martin Schulz, Roger Pearce, Maya Gokhale and Satoshi Matsuoka "Accelerating Big Data Infrastructure and Applications (Ongoing collaboration)", The 1st US-Japan Workshop Enabling Global Collaborations in Big Data Research (ICDCS2017 workshop), Atlanta, Georgia, Jun 2017, 査読有

[14] Zhigeng Xu, James Lin and Satoshi Matsuoka "Benchmarking Sunway SW26010 Manycore Processor", The Seventh International Workshop on Accelerators and Hybrid Exascale Systems (AsHES), in conjunction with IPDPS 2017, Orlando, Florida USA, May 2017, 査読有

[15] Shweta Salaria, Kevin Brown, Hideyuki Jitsumoto, Satoshi Matsuoka "Evaulation of HPC-Big Data Applications Using Cloud Platforms", 1st Workshop on the Integration of Extreme Scale Computing and Big Data Management and Analytics (EBDMA 2017), in conjunction with CCGrid 2017, Madrid, Spain, May 2017, 査読有

[16] S. Matsuoka, H. Amano, K. Nakajima, K. Inoue, T. Kudoh, N. Maruyama, K. Taura, T. Iwashita, T. Katagiri, T. Hanawa, T. Endo. From FLOPS to BYTES: Disruptive Change in High-Performance Computing towards the Post-Moore Era. In proceedings of the ACM International Conference on Computing Frontiers (CF16), pp. 274 - 281, May 2016. (Invited paper)

[17] Yosuke Oyama, Akihiro Nomura, Ikuro Sato, Hiroki Nishimura, Yukimasa Tamatsu, Satoshi Matsuoka, "Predicting Statistics of Asynchronous SGD Parameters for a Large-Scale Distributed Deep Learning System on GPU Supercomputers", Proc. 2016 IEEE International Conference on Big Data (BigData 2016), Dec. 2016. 査読有(採択率 18.7%)

[18] Satoshi Matsuoka, Hideharu Amano, Kengo Nakajima, Koji Inoue, Tomohiro Kudoh, Naoya Maruyama, Kenjiro Taura, Takeshi Iwashita, Takahiro Katagiri, Toshihiro Hanawa, Toshio Endo, "From FLOPS to BYTES: Disruptive Change in High-Performance Computing towards the Post-Moore Era", ACM Computing Frontiers 2016 (CF16), Invited Paper, Como, Italy, 2016/5/16

[19] Yusuke Nagasaka, Akira Nukada, Satoshi Matsuoka, "Adaptive Multi-level Blocking Optimization for Sparse Matrix Vector Multiplication on GPU", INTERNATIONAL CONFERENCE ON COMPUTATIONAL SCIENCE(ICCS 2016), San Diego, California, U.S.A., 2016/6/6

[20] Anna Gladkova, Aleksandr Drozd, Satoshi Matsuoka, "Analogy-based detection of morphological and semantic relations with word embeddings: what works and what doesnt.", NAACL 2016 student research workshop, San Diego, 2016/6/13

[21] Pak Markthub, Akihiro Nomura, Satoshi Matsuoka, "Serving More GPU Jobs, with Low Penalty, using Remote GPU Executon and Migration", IEEE Cluster 2016, Taipei, Taiwan, 2016/9/13

[22] Aleksandr Drozd, Olaf Witkowski, Satoshi Matsuoka, Takashi Ikegami, "Critical Mass in the Emergence of Collective Intelligence: A Parallelized Simulation of Swarms in Noisy Environments", Artificial Life and Robotics, Volume 21 Issue 3, September 2016, Pages 317-323

[23] Abdelhalim Amer, Satoshi Matsuoka, Miquel Pericas, Naoya Maruyama, Kenjiro Taura, Rio Yokota, and Pavan Balaji, "Scaling FMM with Data-Driven OpenMP Tasks on Multicore Architectures", IWOMP2016, Kyoto, Japan, 2016/10/5

[24] Jian Guo, Kun Qian, Huijie Xu, Christoph Janott, Bjorn Schuller, Satoshi Matsuoka, "GPU-Based Fast Signal Processing for Large Amounts of Snore Sound Data", IEEE GCCE 2016, Kyoto, Japan, 2016/10/11

[25] Hamid Reza Zohouri, Naoya Maruyama, Aaron Smith, Motohiko Matsuda, Satoshi Matsuoka, "Evaluating and Optimizing OpenCL Kernels for High Performance Computing with FPGAs", The International Conference for High Performance Computing, Networking, Storage and Analysis (SC|16), Salt Lake City, UT, USA, 2016/11/13
[26] Mateusz Bysiek, Aleksandr Drozd, Satoshi Matsuoka, "Migrating legacy Fortran to Python while retaining Fortran-level performance through transpilation and type hints", 6th PyHPC workshop in conjunction with SC16, Salt Lake City, USA, 2016/11/14
[27] Yosuke Oyama, Akihiro Nomura, Ikuro Sato, Hiroki Nishimura, Yukimasa Tamatsu, Satoshi Matsuoka, "Predicting Statistics of Asynchronous SGD Parameters for a Large-Scale Distributed Deep Learning System on GPU Supercomputers", IEEE Big Data 2016, Washington D.C., USA, 2016/12/5
[28] Koji Ueno, Naoya Maruyama, Toyotaro Suzumura, Satoshi Matsuoka, "Efficient Breadth-First Search on Massively Parallel and Distributed Memory Machines", IEEE Big Data 2016, Washington D.C., USA, 2016/12/5
[29] Hiroki Kanezashi and Toyotaro Suzumura, "An Incremental Local-First Community Detection Method for Dynamic Graphs", 2016 BigGraphs Workshop (IEEE Big Data 2016), Washington D.C., USA, 2016/12/5
[30] Tianqi Xu, Kento Sato, Satoshi Matsuoka, "CloudBB: Scalable I/O Accelerator for Shared Cloud Storage", ICPADS 2016, Wuhan, China, 2016/12/13
[31] Keisuke Fukuda, Motohiko Matsuda, Naoya Maruyama, Rio Yokota, Kenjiro Taura, Satoshi Matsuoka, "Tapas: An Implicitly Parallel Programming Framework For Hierarchical N-body Algorithms", ICPADS 2016, Wuhan, China, 2016/12/13
[32] Aleksandr Drozd, Anna Gladkova, Satoshi Matsuoka, "Word embeddings, analogies, and machine learning: beyond king - man + woman = queen", Coling 2016, Osaka, Japan, 2016/12/15
[33] Manu Shantharam, Keita Iwabuchi, Pietro Cicotti, Laura Carrington, Maya Gokhale, Roger Pearce, "Performance evaluation of scale-free graph algorithms", NVM Workshop, San Diego, 2017/3/12
[34] Kun Qian, Jian Guo, Ken Ishida, Satoshi Matsuoka, "Fast Recognition of Bird Sounds Using Extreme Learning Machine", IEEJ Transactions on Electronics, Information and Systems, Volume 12, Issue 2, March 2017, Pages 294–296Hamid Reza Zohouri, Naoya Maruyama, Aaron Smith, Motohiko Matsuda, Satoshi Matsuoka, "Evaluating and Optimizing OpenCL Kernels for High Performance Computing with FPGAs", International Conference for High Performance Computing, Networking, Storage, and Analysis (IEEE/ACM SC16), Salt Lake City, UT, USA, Nov. 2016. 査読有(採択率 18.3%)
[35] Hideyuki Shamoto, Koichi Shirahata, Aleksandr Drozd, Hitoshi Sato, Satoshi Matsuoka "GPU-Accelerated Large-scale Distributed Sorting Coping with Device Memory Capacity", IEEE Transactions on Big Data, 2016/1/5
[36] "Hamid Reza Zohouri, Naoya Maruyama, Aaron Smith, Motohiko Matsuda, Satoshi Matsuoka ""Towards Understanding the Performance of FPGAs using OpenCL Benchmarks"", 10th HiPEAC Workshop on Reconfigurable Computing (WRC'16), Prague, Czech, 2016/1/18
[37] Keita Iwabuchi, Roger Pearce, Brian Van Essen, Maya Gokhale, Satoshi Matsuoka "Design of a NVRAM Specialized Degree Aware Dynamic Graph Data Structure", 7th Annual Non-Volatile Memories Workshop 2016, San Diego, USA, 2016/3/6
[38] 上野 晃司, 鈴村 豊太郎, 松岡 聡 "Pregel グラフ処理系におけるメッセージ配送最適化", 情報処理学会論文誌コンピューティングシステム（ACS）, 9(1), pp. 30-40, 2016/3/8, ISSN 1882-7829
[39] James Lin, Akira Nukada, Satoshi Matsuoka "Modeling Gather and Scatter with Hardware Performance Counters for Xeon Phi", The 15th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGRID 2015) Doctoral Symposium, Shenzhen, Guangdong, China, IEEE Press, 2015
[40] Abdelhalim Amer, Huiwei Lu, Pavan Balaji, Satoshi Matsuoka "Characterizing MPI and Hybrid MPI+Threads Applications at Scale: Case Study with BFS", The 2nd Workshop on Parallel Programming Model for the Masses (PPMM 2015) in conjunction with the 15th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGRID 2015), Shenzhen, Guangdong, China, IEEE Press, 2015
[41] Chaojie Zhang, Koichi Shirahata, Shuji Suzuki, Yutaka Akiyama, Satoshi Matsuoka "Performance Analysis of MapReduce Implementations for High Performance Homology Search", 2015 年ハイパフォーマンスコンピューティングと計算科学シンポジウム, 東京, 2015/5/19
[42] 高嵜 祐樹, 遠藤 敏夫, 松岡 聡 "GPU クラスタにおける大規模都市気流シミュレーションの最適化と性能モデル", 2015 年ハイパフォーマンスコンピューティングと計算科学シンポジウム, 東京, 2015/5/20
[43] Taichiro Suzuki, Akira Nukada, Satoshi Matsuoka "Efficient Execution of Multiple CUDA Applications using Transparent Suspend, Resume and Migration", Euro-Par 2015 Parallel Processing, Vienna, Austria, 2015/8/26

[44] Aleksandr Drozd, Olaf Witkowski, Satoshi Matsuoka, Takashi Ikegami "Signal-Driven Swarming: A Parallel Implementation of Evolved Autonomous Agents to Perform A Foraging Task", The First International Symposium on Swarm Behavior and Bio-Inspired Robotics, Kyoto, Japan, 2015/10/28
[45] James Lin, Qiang Qin, Shuo Li, Minhua Wen and Satoshi Matsuoka "Evaluating AVX2 Vgather Instruction with Stencils", HPC China 2015, Wuxi, China, 2015/11/9
[46] Aleksandr Drozd, Anna Gladkova, Satoshi Matsuoka "Python, Performance, and Natural Language Processing", 5th PyHPC workshop @ SC15, Austin, TX, USA, 2015/11/15
[47] Hiroki Kanezashi and Toyotaro Suzumura "Performance Optimization for Agent-Based Traffic Simulation by Dynamic Agent Assignment", Winter Simulation Conference 2015, Huntington Beach, CA, 2015/12/8
[48] Aleksandr Drozd, Anna Gladkova, Satoshi Matsuoka "Discovering Aspectual Classes of Russian Verbs in Untagged Large Corpora", IEEE DSDIS 2015, Sydney, Australia, 2015/12/11
[49] Kevin Brown, Satoshi Matsuoka and Jens Domke "Hardware-Centric Analysis of Network Performance for MPI Applications", 21st IEEE International Conference on Parallel and Distributed Systems (ICPADS 2015), Melbourne, Austrailia, 2015/12/14
[50] Toshio Endo, Yuki Takasaki, Satoshi Matsuoka "Realizing Extremely Large-Scale Stencil Applications on GPU Supercomputers", 21st IEEE International Conference on Parallel and Distributed Systems (ICPADS 2015), Melbourne, Austrailia, 2015/12/14
[51] Abdelhalim Amer, Huiwei Lu, Yanjie Wei, Pavan Balaji, Satoshi Matsuoka, "MPI+Threads: Runtime Contention and Remedies", The 20th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP 2015), The ACM Press, pp. 239-248,Feb 2015, 査読有(採択率 17%)
[52] H. Shamoto, K. Shirahata, A. Drozd, H. Sato, S. Matsuoka, "GPU-Accelerated Large-scale Distributed Sorting Coping with Device Memory Capacity", IEEE Trans. Big Data, Vol. 2, Issue 1, pp. 57-69, 2015. (DOI: 10.1109/TBDATA.2015.2511001) 査読有(ジャーナル)
[53] Aleksandr Drozd, Miquel Pericas and Satoshi Matsuoka, “Efficient String Sorting on Multi- and Many-Core Architectures”, The 2014 IEEE International Congress on Big Data (BigData Congress 2014), 2014. (DOI: 10.1109/BigData.Congress.2014.97)
[54] Miquel Pericas, Kenjiro Taura, Satoshi Matsuoka, “Scalable Analysis of Multicore Data Reuse and Sharing”, International Conference on Supercomputing (ICS14), ACM Press, 2014.
[55] Koichi Shirahata, Jun Doi, Mikio Takeuchi, “Performance Analysis of Lattice QCD Application with APGAS Programming Model”, The 2014 X10 Workshop (X1014) co -located with PLDI14, 2014 .
[56] Koichi Shirahata, Hitoshi Sato, Satoshi Matsuoka, “Out-of-core GPU Memory Management for MapReduce-based Large-scale Graph Processing”, IEEE International Conference on Cluster Computing (CLUSTER2014), 2014. (DOI: 10.1109/CLUSTER.2014.6968748)
[57] Chih-Song Kuo, Aamer Shah, Akihiro Nomura, Satoshi Matsuoka and Felix Wolf, “How File Access Patterns Influence Interference Among Cluster Applications”, IEEE International Conference on Cluster Computing (CLUSTER2014), 2014. (DOI: 10.1109/CLUSTER.2014.6968743)
[58] Hideyuki Shamoto, Koichi Shirahata, Aleksandr Drozd, Hitoshi Sato and Satoshi Matsuoka, “Large-scale Distributed Sorting for GPU-based Heterogeneous Supercomputers ”, The IEEE International Conference on Big Data 2014 (IEEE BigData 2014) pp.510-518, 2014. (DOI: 10.1109/BigData.2014.7004268)
[59] Keita Iwabuchi, Hitoshi Sato, Yuichiro Yasui, Katsuki Fujisawa and Satoshi Matsuoka, “NVM-based Hybrid BFS with memory efficient data structure”, The IEEE International Conference on Big Data 2014 (IEEE BigData 2014) pp.529-538, 2014. (DOI: 10.1109/BigData.2014.7004270)
[60] Jens Domke, Torsten Hoefler, Satoshi Matsuoka, “Fail-in-place network design: interaction between topology, routing algorithm and failures” International Conference for High Performance Computing, Networking, Storage and Analysis (SC14)
[61] Toshio Endo, Akira Nukada and Satoshi Matsuoka, “TSUBAME-KFC: a Modern Liquid Submersion Cooling Prototype towards Exascale Becoming the Greenest Supercomputer in the World”, The 20th IEEE International Conference on Parallel and Distributed Systems (ICPADS 2014), pp. 360-367, 2014.
[62] Yusuke Nagasaka, Akira Nukada, Satoshi Matsuoka, “Cache-Aware Sparse Matrix Formats for Kepler GPU”, The 20th IEEE International Conference on Parallel and Distributed Systems (ICPADS 2014), pp. 281-288, 2014.
[63] Jens Domke, Torsten Hoefler, Satoshi Matsuoka, "Fail-in-place network design: interaction between topology, routing algorithm and failures", IEEE/ACM International Conference for High Performance Computing, Networking, Storage and Analysis (SC14), The IEEE Press, pp.597-608, Nov. 2014, 査読有 (採択率 19%)
[64] S. Matsuoka, H. Sato, O. Tatebe, F. Takatsu , M. Jabri, M. Koibuchi, I. Fujiwara, S. Suzuki, M. Kakuta, T. Ishida,

Y. Akiyama, T. Suzumura, K. Ueno, H. Kanezashi, and T. Miyoshi, “Extreme Big Data (EBD): Next Generation Big Data Infrastructure Technologies Towards Yottabyte/Year”, Supercomputing Frontiers and Innovations Vol.1, No.2, pp. 89-107, 2014. (DOI: 10.14529/jsfi140206) 査読有(ジャーナル)

[65] K. Sato, K. Mohror, A. Moody, T. Gamblin, B. Supinski, N. Maruyama, S. Matsuoka, "A User-level InfiniBand-based File System and Checkpoint Strategy for Burst Buffers", the 14th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGrid2014), The IEEE Press, May, 2014, pp. 21-30, 査読有(採択率19%), **The Best Paper Award 受賞**

[66] Keita Iwabuchi, Hitoshi Sato, Ryo Mizote, Yuichiro Yasui, Katsuki Fujisawa and Satoshi Matsuoka, “Hybrid BFS Approach Using Semi-External Memory” IPDPS-HPDIC2014 3rd High Performance Data Intensive Computing Workshop , Phoenix, Arizona, USA, May 23, 2014

[67] Kento Sato, Adam Moody, Kathryn Mohror, Todd Gamblin, Bronis R. de Supinski, Naoya Maruyama and Satoshi Matsuoka, "FMI: Fault Tolerant Messaging Interface for Fast and Transparent Recovery", In Proceedings of the International Conference on Parallel and Distributed Processing Symposium 2014 (IPDPS2014), Phoenix, USA, May, 2014

[68] Miquel Pericàs, Abdelhalim Amer, Kenjiro Taura and Satoshi Matsuoka "Analysis of Data Reuse in Task-Parallel Runtimes" 4th International Workshop on Performance Modeling, Benchmarking and Simulation of High Performance Computer Systems (PMBS13), Denver, November 2013

[69] Takafumi Saito, Kento Sato, Hitoshi Sato and Satoshi Matsuoka, “Energy-aware I/O Optimization for Checkpoint and Restart on a NAND Flash Memory System”, In Proceedings of FTXS2013, the Workshop on Fault -Tolerance for HPC at Extreme Scale, in conjunction with the 22nd International ACM Symposium on High Performance Parallel and Distributed Computing (HPDC13), ACM Press, 2013

[70] Koichi Shirahata, Hitoshi Sato, Toyotaro Suzumura and Satoshi Matsuoka, “A Scalable Implementation of a MapReduce-based Graph Processing Algorithm for Large-scale Heterogeneous Supercomputers”, In Proceedings of the 13th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGrid 2013), the IEEE Press, pp.277-284, 2013

[71] Satoshi Matsuoka, Takayuki Aoki, Toshio Endo, Hitoshi Sato, Shin’ichiro Takizawa, Akihiro Nomura, and Kento Sato. “TSUBAME2.0: The First Petascale Supercomputer in Japan and the Greenest Production in the World”, in Contemporary High Performance Computing: From Petascale Towards Exascale, J. Vetter (ed.), Chapman & Hall Computational Science Series, pp. 525-556, 2013.

[72] Koichi Shirahata, Hitoshi Sato, Toyotaro Suzumura, Satoshi Matsuoka. “A GPU Implementation of Generalized Graph Processing Algorithm GIM-V”. In Proc. of the 3rd International Workshop on Parallel Algorithm and Parallel Software (IWPAPS 2012), in conjunction with Cluster 2012, Sep. 2012.

[73] Akihiro Nomura, Yutaka Ishikawa, Naoya Maruyama, Satoshi Matsuoka. “Design and Implementation of Portable and Efficient Non-blocking collective Communication”.. In The 12th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGrid 2012). May 2012.

[74] Amer Abdelhalim, Toufik Ahmed, Hidouci Walid-Khaled and Satoshi Matsuoka, “Using Bittorrent and SVC for Efficient Video Sharing and Streaming”, In Proc of the Seventeenth IEEE Symposium on Computers and Communications (ISCC’12), IEEE Press, Jul. 2012.

[75] Irina Demeshko, Satoshi Matsuoka, Naoya Maruyama, Hirofumi Tomita. “Ultra-high Resolution Atmospheric Global Circulation Model NICAM on Graphics Processing Unit”, In Proc. of the 2012 International Conference on Parallel and Distributed Processing Techniques and Applications (PDTPA’12), Jul. 2012.

[76] Irina Demeshko, Satoshi Matsuoka, Naoya Maruyama and Hirofumi Tomita. “Multi-GPU implementation of the NICAM atmospheric model”, In Proc. of Tenth International Workshop on Algorithms, Models and Tools for Parallel Computing on Heterogeneous Platforms (HeteroPar’2012) in conjunction with EuroPar’2012, Aug. 2012.

[77] L. Bautista Gomez, B. Nicolae, N. Maruyama, F. Cappello, S. Matsuoka. “Scalable Reed-Solomon-based Reliable Local Storage for HPC Applications on IaaS Clouds”,

[78] Leonardo Bautista Gomez, Thomas Ropars, Naoya Maruyama, Franck Cappello, Satoshi Matsuoka. “Hierarchical Clustering Strategies for Fault Tolerance in Large Scale HPC Systems”, In Proc. of IEEE Cluster 2012, IEEE Press, Sep. 2012

[79] Akira Nukada, Kento Sato and Satoshi Matsuoka. “Scalable Multi-GPU 3-D FFT for TSUBAME 2.0 Supercomputer”, In Proc. of 2012 ACM/IEEE International Conference for High Performance, Networking, Storage, and Analysis (SC’12), Salt Lake City, IEEE Press, Nov. 2012.

[80] Kento Sato, Adam Moody, Kathryn Mohror, Todd Gamblin, Bronis R.de Supinski, Naoya Maruyama, Satoshi Matsuoka. “Design and Modeling of a Non-blocking Checkpointing System”, In Proc. of 2012 ACM/IEEE

International Conference for High Performance, Networking, Storage, and Analysis (SC'12), Salt Lake City, IEEE Press, Nov. 2012.

[81] Katsuki Fujisawa, Toshio Endo, Hitoshi Sato, Makoto Yamashita, Satoshi Matsuoka, Maho Nakata. "High-Performance General Solver for Extremely Large-scale Semidefinite Programming Problems", In Proc. of 2012 ACM/IEEE International Conference for High Performance, Networking, Storage, and Analysis (SC'12), Salt Lake City, IEEE Press, Nov. 2012.

[82] Leonardo Bautista Gomez, Bogdan Nicolae, Naoya Maruyama, Franck Cappello, and Satoshi Matsuoka, "Scalable Reed-Solomon-based reliable local storage for HPC applications on iaas clouds", In Proceedings of the 18th international conference on Parallel Processing (Euro-Par12), Springer, pp.313 -324, 2012

[83] Kento Sato, Naoya Maruyama, Kathryn Mohror, Adam Moody, Todd Gamblin, Bronis R. de Supinski, and Satoshi Matsuoka, "Design and modeling of a non-blocking checkpointing system", In Proceedings of the International Conference on High Performance Computing, Networking, Storage and Analysis (SC 12), pp.19:1 -19:10, 2012

[84] Akira Nukada, Kento Sato and Satoshi Matsuoka. "Scalable Multi-GPU 3-D FFT for TSUBAME 2.0 Supercomputer", In Proc. of 2012 ACM/IEEE International Conference for High Performance, Networking, Storage, and Analysis (SC'12), Salt Lake City, IEEE Press, pp.1-10, Nov. 2012, 査読有(採択率 21%)

[85] L. Bautista, N. Maruyama, D. Komatitsch, T. Seiji, F. Cappello, S. Matsuoka, N. Takeshi. "FTI: High performance Fault Tolerance Interface for hybrid systems". In ACM/IEEE Supercomputing (SC2011), pp.1-12, Nov. 2011, 査読有(採択率 21%) **特別賞 Special Recognition Award for Perfect Score Award 受賞**

[86] Naoya Maruyama, Tatsuo Nomura, Kento Sato and Satoshi Matsuoka, "Physis: An Implicitly Parallel Programming Model for Stencil Computations on Large-Scale GPU-accelerated Supercomputers" In Proceedings of the 2010 ACM/IEEE conference on Supercomputing (SC11), pp.11:1 -11:12, 2011

[87] Mohamed Amin JABRI and Satoshi MATSUOKA. "Dealing with Grid-Computing Authorization using Identity-Based Certificateless Proxy Signature", In Proceedings of the 11th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGrid 2011), pp. 544--553, Newport Beach, CA, May 2011.doi=10.1109/CCGrid.2011.12

[88] 遠藤 敏夫, 額田 彰, 松岡 聡. スーパーコンピュータ TSUBAME 2.0 における Linpack 性能 1 ペタフロップス超の達成. 情報処理学会論文誌コンピューティングシステム, Vol. 4, No.4 (ACS 35), pp.169–179, 2011 年 10 月.

[89] 滝澤 真一朗,松岡 聡,友石 正彦,佐藤 仁,東田 学.Point-of-Presence 連携による e-サイエンス分散環境..In インターネットカンファレンス 2011.Oct. 2011.

[90] Shuntaro Yamazaki, Akira Nukada, Masaaki Mochimaru, "Hamming Color Code for Dense and Robust One-shot 3D Scanning", In Proc. of the 2011 British Machine Vision Conference, Dundee, Scotland, Springer, Aug. 2011.

[91] Akira Nukada, Hiroyuki Takizawa, Satoshi Matsuoka. NVCR: A Transparent Checkpoint-Restart Library for NVIDIA CUDA. Proceedings of the 20th International Heterogeneity in Computing Workshop (HCW 2011), in conjunction with IEEE IPDPS 2011.The IEEE Press. In The 20th International Heterogeneity in Computing Workshop (HCW 2011), in conjunction with IEEE IPDPS 2011.page 1--10.May. 2011.

[92] 遠藤 敏夫,額田 彰,松岡 聡.スーパーコンピュータ TSUBAME 2.0 における Linpack 性能 1 ペタフロップス超の達成.情報処理学会 SACSIS2011 論文集.情報処理学会.In 先進的計算基盤システムシンポジウム(SACSIS2011).pp. 1-8.May. 2011.

[93] Sumeth Lerthirunwong, Hitoshi Sato, Satoshi Matsuoka. "Multi-ring Structured Overlay Network for the Inter-cloud Computing Environment", In Proceedings of the 1st International Conference on Cloud Computing and Services Science (CLOSER 2011), pp. 5--14, Noordwijkerhout, Netherlands, 7-9 May, 2011.

[94] Tatsuhiro Chiba, Mathjis den Burger, Thilo Kielmann and Satoshi Matsuoka, "Dynamic Load-Balanced Multicast for Data-Intensive Applications on Clouds", In Proceedings of the 10th IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGrid 2010), pp.5-14, 2010

[95] Koichi Shirahata, Hitoshi Sato, and Satoshi Matsuoka. "Hybrid Map Task Scheduling for GPU-Based Heterogeneous Clusters", In Proceedings of the 2010 IEEE Second International Conference on Cloud Computing Technology and Science (CLOUDCOM 10), pp.733 -740, 2010

[96] Hitoshi Sato, Satoshi Matsuoka, and Toshio Endo, "File Clustering Based Replication Algorithm in a Grid Environment" In Proceedings of the 9th IEEE/ACM International Symposium on Cluster Computing and the Grid (CCGrid 09), pp.204 -211, 2009

[97] Tetsuya Hoshino, Naoya Maruyama, Satoshi Matsuoka and Ryoji Takaki, "CUDA vs OpenACC: Performance Case Studies with Kernel Benchmarks and a Memory Bound CFD Application", In Proceedings of the 13th

IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGrid 2013), pp.136-143, 2013
[98] Leonardo Bautista Gomez, Thomas Ropars, Naoya Maruyama, Franck Cappello and Satoshi Matsuoka, "Hierarchical Clustering Strategies for Fault Tolerance in Large Scale HPC Systems", In Proceedings of Cluster Computing (CLUSTER), pp.355-363, 2012
[99] Koichi Shirahata, Hitoshi Sato, Toyotaro Suzumura and Satoshi Matsuoka. “A GPU Implementation of Generalized Graph Processing Algorithm GIM-V”, In Proceedings of the 3rd International Workshop on Parallel Algorithm and Parallel Software (IWPAPS 2012), in conjunction with Cluster 2012, pp.207-212, 2012
[100] Akira Nukada, Hiroyuki Takizawa and Satoshi Matsuoka, “NVCR: A Transparent Checkpoint-Restart Library for NVIDIA CUDA” In Proceedings of the 20th International Heterogeneity in Computing Workshop (HCW 2011), in conjunction with IEEE IPDPS 2011, pp.1-10, 2011
[101] Kento Sato ,Hitoshi Sato, and Satoshi Matsuoka. “Orchestrated Data Processing Acceleration for Data-Intensive Applications by using VM-based Migration”. In The 1st Data Intensive Science Workshop, IEEE Press, Mar. 2011.
[102] Naoya Maruyama, Akira Nukada, and Satoshi Matsuoka. “A High-Performance Fault-Tolerant Software Framework for Memory on Commodity GPUs”, In Proceedings of 24th IEEE International Parallel and Distributed Processing Symposium (IPDPS10), IEEE Press, pp.1-12, 2010
[103] Ali Cevahir, Akira Nukada and Satoshi Matsuoka, “High Performance Conjugate Gradient Solver on Multi-GPU Clusters Using Hypergraph Partitioning”, Computer Science - Research and Development, Vol.25, No.1-2, pp.83-91, Springer-Verlag, 2010
[104] Toshio Endo, Akira Nukada, Satoshi Matsuoka, and Naoya Maruyama. Linpack Evaluation on a Supercomputer with Heterogeneous Accelerators, In Proceedings of IEEE International Parallel & Distributed Processing Symposium (IPDPS 2010), Atlanta, April 2010..
[105] Ali Cevahir, Akira Nukada, and Satoshi Matsuoka. “High Performance Conjugate Gradient Solver on Multi-GPU Clusters Using Hypergraph Partitioning” In Proceedings of the 2010 International Supercomputing Conference (ISC10), Hamburg,Germany, June 2010.
[106] Leonardo Bautista Gomez, Akira Nukada, Naoya Maruyama, Franck Cappello and Satoshi Matsuoka. Low-overhead diskless checkpoint for hybrid computing systems. In Proceedings of 2010 High Performance Computing Conference (HiPC 2010), Goa, Dec. 2010.
[107] Kento Sato, Hitoshi Sato and Satoshi Matsuoka, "A Model-Based Algorithm for Optimizing I/O Intensive Applications in Clouds using VM-Based Migration”, In Proceedings of Cloud2009 in conjunction with the 9th IEEE/ACM International Symposium on Cluster Computing and the Grid (CCGrid2009), pp.466-471, 2009
[108] Tomoaki Hamano, Toshio Endo, and Satoshi Matsuoka. Power-Aware Dynamic Task Scheduling for Heterogeneous Accelerated Clusters. In Proceedings of the Fifth Workshop on High-Performance, Power-Aware Computing (HPPAC), in conjunction to IEEE IPDPS 2009, Rome, May 2009.
[109] Ali Cevahir, Akira Nukada, and Satoshi Matsuoka. Fast Conjugate Gradients with Multiple GPUs. In Proceedings of International Conference on Computer Science (ICCS 2009), Lecture Notes in Computer Science, Vol. 5544, pp.893-903, Springer, May 2009.
[110] 額田彰, 松岡聡. CUDA GPU 向けの自動最適化 FFT ライブラリ. 先進的基盤システムシンポジウム SACSIS 2009 論文集、pp. 345-352, 広島、2009 年 5 月.
[111] Ali Cevahir, Akira Nukada, and Satoshi Matsuoka. An Efficient Conjugate Gradient Solver on Double Precision Multi-GPU Systems. 先進的基盤システムシンポジウム SACSIS 2009 論文集、pp. 353-360, 広島、2009 年 5 月.
[112] Satoshi Matsuoka Takayuki Aoki Toshio Endo Akira Nukada Toshihiro Kato, Atsushi Hasegawa, GPU accelerated computing–from hype to mainstream, the rebirth of vector computing, Journal of Physics: Conference Series, Scientific Discovery through Advanced Computing (SciDAC 2009), Vol. 180, No. 1, pp. 012043, San Diego, CA, July 2009.
[113] 額田 彰, 松岡 聡, CUDA GPU 向けの自動最適化 FFT ライブラリ, 情報処理学会論文誌コンピューティングシステム(ACS), Vol. 2, No. 3, pp. 107-115, 2009 年 9 月.
[114] Akira Nukada, Satoshi Matsuoka, Auto-Tuning 3-D FFT Library for CUDA GPUs, In Proceedings of the 2009 ACM/IEEE conference on Supercomputing (SC09), Portland, OR, November 2009.
[115] Sumeth Lerthirunwong, Naoya Maruyama, and Satoshi Matsuoka. Index Distribution Technique for Efficient Search on Unstructured Peer-to-Peer Networks. In the International Conference in Electrical Engineering/Electronics, Computer, Telecommunications, and Information Technology (ECTI-CON 2008), May 2008.

[116] Yuto Hosogaya and Toshio Endo and Satoshi Matsuoka. Performance Evaluation of Parallel Applications on Next Generation Memory Architecture with Power-Aware Paging Method. In Proceedings of 4th IEEE Workshop on High-Performance, Power-Aware Computing (HPPAC08), in conjunction with IPDPS2008, 8pages, April 2008.
[117] Toshio Endo and Satoshi Matsuoka. Massive Supercomputing Coping with Heterogeneity of Modern Accelerators. In Proceedings of IEEE International Parallel & Distributed Processing Symposium (IPDPS 2008), 10pages, April 2008.
[118] Yasuhiko Ogata, Toshio Endo, Naoya Maruyama, and Satoshi Matsuoka. "An Efficient, Model-Based CPU-GPU Heterogeneous FFT Library". In the 17th International Heterogeneity in Computing Workshop (HCW08), in conjunction with IPDPS 2008, Miami, FL, USA, April 2008.
[119] 尾形 泰彦,遠藤 敏夫,丸山 直也,松岡 聡. 性能モデルに基づく CPU 及び GPU を併用する効率的な FFT ライブラリ. 情報処理学会論文誌コンピューティングシステム,Vol.1,No.1 (ACS 22), pp. 40-50,2008 年 6 月.
[120] 額田彰,尾形泰彦,遠藤敏夫,松岡聡. CUDA 環境における高性能 3 次元 FFT. 情報処理学会論文誌コンピューティングシステム（ACS）,Vol. 1,No. 2,pp. 231-239,2008 年 8 月.
[121] Akira Nukada, Yasuhiko Ogata, Toshio Endo and Satoshi Matsuoka. Bandwidth Intensive 3-D FFT kernel for GPUs using CUDA. In Proceedings of the ACM/IEEE conference on Supercomputing (SC08), 11pages, Austin, November 2008.
[122] Hideo Nishimura, Naoya Maruyama, and Satoshi Matsuoka. “Virtual Clusters on the Fly--Fast, Scalable, and Flexible Installation”, The 7th IEEE International Symposium on Cluster Computing and the Grid (CCGrid 2007), The IEEE Press, May, 2007 (to appear).
[123] Hideyuki Jitsumoto, Toshio Endo, and Satoshi Matsuoka. “ABARIS: An Adaptable Fault Detection/Recovery Component Framework for MPIs”. In the 12th IEEE Workshop on Dependable Parallel, Distributed and Network-Centric Systems (DPDNS’07), The IEEE Press (CD-ROM Publication）, March, 2007.
[124] 遠藤 敏夫, 松岡聡, 橋爪信明, 長坂真路. 「ヘテロ型スーパーコンピュータ TSUBAME の Linpack による性能評価」 2007 年ハイパフォーマンスコンピューティングと計算科学シンポジウム、情報処理学会 HPCS2007 論文集, pp. 33-40, 2007 年 1 月.
[125] Masaki Tatezono, Naoya Maruyama, and Satoshi Matsuoka. “Making Wide-Area, Multi-Site MPI Feasible Using Xen VM”, In Workshop on XEN in HPC Cluster and Grid Computing Environments (XHPC06), Frontiers of High Performance Computing and Networking, Frontiers of High Performance Computing and Networking, Sorrento, Italy, Dec. 2006.
[126] 松岡聡. ＴＳＵＢＡＭＥの飛翔: ペタスケールへ向けた「みんなのスパコン」の構想. 情報処理学会研究報告 2006-HPC-107, pp.37-42., 2006 年 8 月.
[127] Yoshihiko Hotta, Mitsuhisa Sato, Hideaki Kimura , Satoshi Matsuoka , Taisuke Boku, Daisuke Takahashi. “Profile-based Optimization of Power-Performance by using Dynamic Voltage Scaling on a PC cluster”, The Second IPDPS Workshop on High-Performance, Power-Aware Computing April, 2006, Rhodes, Greece, The IEEE Press (CD-ROM Publication).
[128] Taisuke Boku, Mitsuhisa Sato, Daisuke Takahashi, Hiroshi Nakashima, Hiroshi Nakamura, Satoshi Matsuoka , Yoshihiko Hotta. “MegaProto/E: Power-Aware High-Performance Cluster with Commodity Technology”, the Second IPDPS Workshop on High-Performance, Power-Aware Computing April, 2006, Rhodes, Greece, the IEEE Press (CD-ROM Publication).
[129] Tatsuhiro Chiba, Toshio Endo, and Satoshi Matsuoka. “High-Performance MPI Broadcast Algorithm for Grid Environments Utilizing Multi-lane NICs” The 7th IEEE International Symposium on Cluster Computing and the Grid (CCGrid 2007), The IEEE Press, May, 2007 (to appear).
[130] Laurent Baduel and Satoshi Matsuoka. Outil autonome de surveillance de grilles. French journal of Infomation Sciences and Technologies, Special edition on P2P Information Systems, 2007 (to appear)
[131] Norihiro Umeda, Hidemoto Nakada, Satoshi Matsuoka. “Peer-to-Peer Scheduling System with Scalable Information Sharing Protocol”, Proc. Middleware Architecture in the Internet (The SAINT 2007 workshops), Jan, 2007, (CD-ROM Publication), Jan, 2007.
[132] Yuya Machida, Shinnichiro Takizawa, Hidemoto Nakada, and Satoshi Matsuoka. “Multi-Replication with Intelligent Staging in Data-Intensive Grid Applications”, In the 7th IEEE/ACM International Conference on Grid Computing, the ACM Press, pp. 88-95., Oct. 2006.
[133] 立薗真樹, 中田秀基, 松岡聡. 「仮想計算機を用いたグリッド上での MPI 実行環境」、先進的計算基盤システムシンポジウム SACSIS2006 論文集, pp.525-532, May 2006.
[134] 高宮安仁, 山形育平, 青木孝文, 中田秀基, 松岡聡. 「ORE Grid：仮想計算機を用いたグリッド実行環

境の高速な配置ツール」、先進的計算基盤システ ムシンポジウム SACSIS2006 論文集, pp.541-550, May 2006.
[135] Ikuhei Yamagata, Satoshi Matsuoka, and Hidemoto Nakada. “Speculative Checkpointing”, Proceedings of DSW`06, IPSJ, Jan, 2006.
[136] 中島浩／中村宏／佐藤三久／朴泰祐／松岡聡／高橋大介／堀田義彦. 「高性能計算のための低電力・高密度クラスタ MegaProto」, 情報処理学会論文誌（トランザクション）コンピューティングシステム Vol.46 No.SIG12 46-61 ページ, 2005 年 8 月
[137] Yoshiaki Sakae, Satoshi Matsuoka, Mitsuhisa Sato, Hiroshi Harada, Implementation and Evaluation of Dynamic Load Balancing using Loop Re-partitioning and Page Migration on Omni/SCASH, Proc. 4th International Workshop on OpenMP: Experiences and Implementations, (WOMPEI2005), Jan., 2005.
[138] Hiroshi Nakashima, Hiroshi Nakamura, Mitsuhisa Sato, Taisuke Boku, Satoshi Matsuoka, Daisuke Takahashi, and Yoshihiko Hotta. “MegaProto: 1 TFlops/10 kW Rack is Feasible Even with Only Commodity Technology”, Proc. IEEE/ACM Supercomputing 2005, the IEEE Computer Society Press, Nov. 2005 (IEEE CD-ROM Publication).
[139] Satoshi Matsuoka. “Low Power Computing for Fleas, Mice, and Mammoth: Do They Speak the Same Language?”, The NSF CTWatch Quatery Journal, Vol. 1 No. 3, pp.2-11, Aug. 2005.
[140] Hiroshi Nakashima, Hiroshi Nakamura, Mitsuhisa Sato, Taisuke Boku, Satoshi Matsuoka, Daisuke Takahashi, and Yoshihiko Hotta. “MegaProto: A Low-Power and Compact Cluster for High-Performance Computing”, the IPDPS Workshop on High-Performance, Power-Aware Computing April, 2005, Denver, Colorado, Omni Interlocken Resort, USA, the IEEE Press (CD-ROM Proceedings)
[141] Kenichiro Shirose, Satoshi Matsuoka, Hidemoto Nakada , and Hirotaka Ogawa. “Autonomous Configuration of Grid Monitoring Systems”, Proc. 2004 International Symposium on Applications and the Internet (SAINT 2004 Workshops), pp.651-657, Jan., 2004.
[142] Hidemoto Nakada, Yoshio Tanaka, Satoshi Matsuoka, Satoshi Sekiguchi "The Design and implementation of a Fault-Tolerant RPC system: Ninf-C", Proceedings of HPC ASIA 2004, pp.9-18, July 21, 2004
[143] Toyotaro Suzumura, Hidemoto Nakada, Satoshi Matsuoka, Henri Casanova "GridSpeed: A Web-based Grid Portal Generation Server", Proceedings of HPC Asia 2004, pp.26-33, July 2004
[144] Atif Shahab, Danny Chuon, Toyotaro Suzumua, Wilfred W. Li, Robert W. Byrnes, Kouji Tanaka, Larry Ang, Satoshi Matsuoka, Philip E. Bourne, Mark A. Miller, Peter W. Arzberger "Grid Portal Interface for Interactive Use and Monitoring of High-Throughput Proteome Annotation", Proceedings of the First International Workshop on Life Science Grid (LSGRID2004), pp. 63-74, May 31-July 1, 2004.
[145] 中田秀基, 田中良夫, 松岡聡, 関口智嗣 "耐故障性を重視した RPC システム Ninf-C の設計と実装", 先進的計算基盤システムシンポジウム SACSIS2004 論文集, pp.77-84, May 2004.
[146] A. Takefusa, O. Tatebe, S. Matsuoka, and Y. Morita "Performance Analysis of Scheduling and Replication Algorithms on Grid Datafarm Architecture for High-Energy Physics Applications", Proc. of the 12th IEEE International Symposium on High Performance Distributed Computing (HPDC-12), IEEE Press, pp. 34-43, June, 2003
[147] Yoshio Tanaka, Hidemoto Nakada, Satoshi Sekiguchi, Toyotaro Suzumura, Satoshi Matsuoka "Ninf-G: A Reference Implementation of RPC-based Programming Middleware for Grid Computing", Journal of Grid Computing, Vol. 1, No. 1, pp. 41-51, Kluwer Academic Publishers, June, 2003.
[148] 栄純明, 松岡聡, 佐藤三久, 原田浩 "Omni/SCASH のループ再分割を用いた動的負荷分散拡張の実装と評価", 先進的計算基盤システムシンポジウム SACSIS2003 論文集, pp.307-314, May 2003.
[149] 高宮安仁, 真鍋篤, 松岡聡 "Lucie: 大規模クラスタに適した高速セットアップ・管理ツール", 先進的計算基盤システムシンポジウム SACSIS2003 論文集, pp.365-372, May 2003.
[150] Yoshiaki Sakae, Satoshi Matsuoka, Mitsuhisa Sato, Hiroshi Harada "Preliminary Evaluation of Dynamic Load Balancing Using Loop Re-partitioning on Omni/SCASH", Proceedings of the Third IEEE/ACM International Symposium on Cluster Computing and the Grid (CCGrid 2003), pp. 463-470, 2003.
[151] Shoji Ogura, Satoshi Matsuoka, Hidemoto Nakada "Evaluation of the inter-cluster data transfer on Grid environment", Proceedings of the Third IEEE/ACM International Symposium on Cluster Computing and the Grid (CCGrid 2003), pp. 374-381, 2003.
[152] Osamu Tatebe, Satoshi Sekiguchi, Youhei Morita, Satoshi Matsuoka, Noriyuki Soda "Worldwide Fast File Replication on Grid Datafarm", Proceedings of the 2003 Computing in High Energy and Nuclear Physics (CHEP03), La Jolla, Ca, USA, March 2003.
[153] Y. Morita, H. Sato, Y. Watase, O. Tatebe, S. Sekiguchi, S. Matsuoka, N. Soda, A. DellAcqua "Building A High

Performance Parallel File System Using Grid Datafarm and ROOT I/O", Proceedings of the 2003 Computing in High Energy and Nuclear Physics (CHEP03), La Jolla, Ca, USA, March 2003.

[154] 建部 修見、森田 洋平、松岡 聡、関口 智嗣、曽田 哲之 "ペタバイトスケールデータインテンシブコンピューティングのための Grid Datafarm アーキテクチャ l", 情報処理学会論文誌 : ハイパフォーマンスコンピューティングシステム, 情報処理学会, Vol.43, No.SIG6, September 2002

[155] 中田秀基, 齊藤真幸, 鈴村豊太郎, 田中良夫, 松岡聡, 関口智嗣 "Grid ポータル構築ツールキット Ninf-Portal", 情報処理学会論文誌 : ハイパフォーマンスコンピューティングシステム, 情報処理学会, Vol.43, No.SIG6, pp. 172-183, September 2002

[156] Toyotaro Suzumura, Hidemoto Nakada, Masayuki Saito, Satoshi Matsuoka, Yoshio Tanaka, Satoshi Sekiguchi "The Ninf Portal : An Automatic Generation Tool for Computing Portals", Joint ACM Java Grande - ISCOPE 2002 Conference, Seattle, Washington,November 3-5, 2002

[157] 建部 修見, 森田 洋平, 松岡 聡, 関口 智嗣, 曽田 哲之 "ペタスケール広域分散データ解析のための Grid Datafarm アーキテクチャ", 情報処理学会論文誌 : ハイパフォーマンスコンピューティングシステム, 情報処理学会, Vol.43, No.SIG (HPS 5), 2002 年 9 月 (to appear)

[158] 岩崎 聖, 松岡 聡, 曽田 哲之, 平野 基孝, 建部 修見, 関口 智嗣 "Grid 環境における大規模クラスタ向けジョブマネージメントアーキテクチャの実装及び性能評価", 情報処理学会研究報告, 2002-ARC-147, HOKKE2002, pp.37-42, 2002 年 3 月

[159] Osamu Tatebe, Youhei Morita, Satoshi Matsuoka, Noriyuki Soda, Satoshi Sekiguchi "Grid Datafarm Architecture for Petascale Data Intensive Computing ", Proceedings of the 2nd IEEE/ACM International Symposium on Cluster Computing and the Grid (CCGrid 2002), pp.102-110, 2002

[160] 笹生 健, 松岡 聡, 建部 修見 "ヘテロなクラスタ環境における並列 LINPACK アルゴリズム", 情報処理学会・電気通信処理学会 並列処理シンポジウム JSPP2002 論文集, pp.71-78, 2002.

[161] 高宮 安仁, 松岡 聡 "ユーザー透過な耐故障製を実現する MPI へ向けて", 情報処理学会・電気通信処理学会 並列処理シンポジウム JSPP2002 論文集, pp.217-224, 2002

[162] Satoshi Shirasuna, Hidemoto Nakada, Satoshi Matsuoka, Satoshi Sekiguchi "Evaluating Web Services Based Implementations of GridRPC", In Proceedings of the 11th IEEE International Symposium on High Performance Distributed Computing (HPDC-11 2002), July 2002

[163] 中田秀基, 齊藤真幸, 鈴村豊太郎, 田中良夫, 松岡聡, 関口智嗣 "Grid ポータル構築ツールキット Ninf-Portal", 情報処理学会・電気通信処理学会 並列処理シンポジウム JSPP2002 論文集, pp. 209-216

[164] 竹房あつ子,松岡聡 "Grid 計算環境におけるデッドラインスケジューリング手法の性能", 情報処理学会 電気通信処理学会 並列シンポジウム JSPP２００１ 論文集 pp 263-270, 2001.06

[165] Kentarou Fukuchi, Satoshi Matsuoka "An Evaluation of Multiple Pointing Input Systems", INTERACT2001 Proceedings

[166] Hiroshi Hosobe, Satoshi Matsuoka "A Foundation of Solution Methods for Constraint Hierarchies", Constraints Journal, Special Issue on Soft Constraints, 2001 (to appear)

[167] Youhei Morita, Osamu Tatebe, Satoshi Matsuoka, Noriyuki Soda, Hiroyuki Sato, Yoshio Tanaka, Satoshi Sekiguchi, S. Kawabata, Yoshiyuki Watase, Masatoshi Imori, Tomio Kobayashi "Grid Data Farm for Atlas Simulation Data Challenges ", Proceedings of International Conference on Computing of High Energy and Nuclear Physics, Peking, China, pp.699-701, Sep. 2001.

[168] Satoshi Matsuoka, Shigeo Itou "Towards performance evaluation of hight-performance computing on multiple Java platforms", Future Generation Computer System 18（2001）281-291

[169] Yukihiko Sohda, Hidemoto Nakada, Satoshi Matsuoka, Hirotaka Ogawa "Implementation of a Portable Software DSM in Java", Proceedings of ACM JavaGrande/ISCOPE 2001,San Francisco, pp.163--172, June, 2001. JavaGrande/ISCOPE 2001 Conference, pp. 163-172, Jun. 2001

[170] Toyotaro Suzumura, Satoshi Matsuoka, Hidemoto Nakada "A Jini-based Computing Portal System", Proceedings of IEEE/ACM Supercomputing 2001, IEEE Computer Society, Denver, Colorado, Nov. 2001

[171] Atsuko Takefusa, Satoshi Matsuoka, Henri Casanova, Fancine Berman "A Study of Deadline Scheduling for Client-Server Systems on the Computational Grid", 10th IEEE International Symposium on High Performance Distributed Computing (HPDC10),IEEE Computer Society Press,pp. 406--415, Aug., 2001.

[172] Yoshiaki Sakae, Satoshi Matsuoka "MPC++ Performance for Commodity Clustering", Proceedings of High Performance Network Computing,Amsterdam, The Netherlands, Springer LNCS No. 2110, pp. 503-512, June, 2001.

[173] Satoshi Matsuoka "Grid RPC meets Data Grid: Network Enabled Services for Data Farming on the Grid", Proceedings of IEEE Symposium on Cluster Computing and the Grid Brisbane, Australia, pp. 13-15, May 2001

(Invited Paper).
[174] Henri Casanova, Satoshi Matsuoka, Jack Dongarra "Network-Enabled Server Systems and the Computational Grid", High Performance Computing Symposium (HPC01),Advanced Simula tion Technologies Conference, April 22-26 in Seattle, Washington (USA), 2001
[175] 中田 秀基,竹房 あつ子,松岡 聡,佐藤 三久,関口 智嗣 "グローバルコンピューティングのためのスケジューリングフレームワーク", 情報処理学会論文誌 Vol.41 No. 5, May 2000, pp.1617-1627.
[176] 早田恭彦,中田 秀基,小川宏高,松岡聡 "Java 向けソフトウエア分散共有メモリの実現", 情報処理学会論文誌 ,Vol.42 No.SIG 3 (PRO10), pp. 12-24, March. 2001.
[177] 中川貴之, 鈴村豊太郎, 松岡聡, 中田秀基 "クライアント・サーバ型のグローバルコンピューティングシステムの比較 - Ninf, NetSolve, CORBA の性能評価 -", 情報処理学会研究会報告 99-HPC-34
[178] Mitsuhisa Sato, Kazuhiro Kusano ,Hidemoto Nakada, Satoshi Sekiguchi, Satoshi Matsuoka "NetCFD: a Ninf CFD component for Global Computing, and its Java applet GUI", Proc. HPC Asia 2000, 2000, pp.501-506.
[179] Satoshi Matsuoka, Hidemoto Nakada, Mitsuhisa Sato, Satoshi Sekiguchi "Design issues of Network Enabled Server Systems for the Grid", Proceedings of GRID 2000: International Workshop on Grid Computing},Bangalore, India, Springer LNCS 1971, pp.4-17, Dec, 2000 (Invited paper)
[180] Hirotaka Ogawa, K. Shimura, Satoshi Matsuoka, Fuyuhiko Maruyama, Yukihiko Sohda, Yasunori Kimura "OpenJIT: An Open-Ended, Reflective JIT Compiler Framework for Java", Springer LNCS 1850: ECOOP 2000 - Object-Oriented Programming (Elisa Bertino Editor), pp. 362-387, August 2000.
[181] Hirotaka Ogawa, K. Shimura, Satoshi Matsuoka, Fuyuhiko Maruyama, Yukihiko Sohda, Yasunori Kimura "OpenJIT Frontend System: an implementation of the reflective JIT compiler frontend", Springer LNCS 1826: Reflection and Software Engineering (Walter Cazzola, Robert J.Stroud, Francesco Tisato Editors), pp. 137-169, June 2000
[182] Shigeo Itou, Satoshi Matsuoka, Hirokazu Hasegawa "AJaPack; A Performance Portable Parallel Java Numerical Library", Proceedings of the ACM 2000 Java Grande Conference, The ACM Press,June, 2000, pp. 140-149.
[183] Toyotaro Suzumura, Takayuki Nakagawa, Satoshi Matsuoka, Hidemoto Nakada, Satoshi Sekiguchi "Are Global Computing Systems Useful? Comparison of Client-server Global Computing Systems Ninf, NetSolve Versus CORBA", 14th IEEE International Parallel ¥& Distributed Processing Symposium, May 2000, pp. 547-556.
[184] 栄 純明, 松岡 聡, 石川 裕, 高橋 俊行 "MPC++-on-MPI のコモディティクラスタ環境における評価", 情報処理学会論文誌 ハイパフォーマンスコンピューティングシステム,Vol.41 No.SIG 8 (HPS2), pp. 60-72, Nov. 2000.
[185] Kento Aida, Atsuko Takefusa, Hidemoto Nakada,Satoshi Matsuoka et. al. "Performance Evaluation Model for Scheduling in Global Computing Systems", The International Journal of High Performance Computing Applications,Sage Science Press, Vol. 14 No.3, Fall 2000, pp. 268-279.
[186] 竹房あつ子, 合田憲人, 松岡聡, 中田秀基, 長浜雲兵 "グローバルコンピューティングのスケジューリングのための性能評価システム", 情報処理学会論文誌, Vol. 41 No. 5, May 2000, pp. 1628-1638.
[187] 小川宏高, 松岡聡, 丸山冬彦, 早田恭彦, 志村浩也 "OpenJIT フロントエンドシステムの設計", 情報処理学会 論文誌, Vol. 41, No. SIG2 (PRO 6), pp. 1-12, March 2000.
[188] 竹房あつ子, 合田憲人、松岡聡 ら "広域計算システムのシミュレーションによる評価", 情報処理学会論文誌 Vol. 40-5, pp. 2192--2202, 1999
[189] Takeo Igarashi, Satoshi Matsuoka, Hidehiko Tanaka "Teddy: A Sketching Interface for 3D Freeform Design", Proc. ACM SIGGRAPH99, Los Angeles, 1999, pp.409 -416. (Impact Paper)
[190] Atsuko Takefusa, Satoshi Matsuoka, HidemotoNakada, Kento Aida, Umpei Nagashima "Overview of a Performance Evaluation System for Global Computing Scheduling Algorithms", Proceedings of 8th IEEE International Symposium on High Performance Distributed Computing (HPDC8)}, pp. 97-104, 1999
[191] Yukihiko Sohda, Hirotaka Ogawa, Satoshi Matsuoka "OMPC++ --- A Portable High-Performance Implementation of DSM using OpenC++ Reflection", Proc. Reflection99,Saint Malo, France, Springer LNCS 1616, pp. 215--234, July, 1999
[192] 丸山 冬彦, 小川 宏高, 松岡 聡 "Java バイトコードをデコンパイルするための効果的なアルゴリズム", 情報処理学会 論文誌:プログラミング(掲載予定)、情報処理学会 論文誌, Vol. 40, No. SIG10 (PRO 5), pp. 39-50, Dec. 1999.
[193] 早田 恭彦, 小川 宏高, 松岡 聡 "OpenC++のリフレクション機能を用いた分散共有メモリの実現", 情報処理学会 論文誌:プログラミング Vol. 40, No. SIG1 (PRO 2),pp. 13--22, 1999
[194] 竹房 あつ子, 合田 憲人, 中田 秀基, 小川 宏高, 松岡 聡, 佐藤 三久, 関口 智嗣, 長嶋 雲兵 "グローバルコンピューティングシステムのシミュレーションによる評価", 情報処理学会 論文誌 Vol. 40-5,

pp. 2192--2202, 1999.

[195] 竹房 あつ子, 合田 憲人, 小川 宏高, 中田 秀基, 松岡 聡, 佐藤 三久, 関口 智嗣, 長嶋 雲兵 "広域計算システムのシュミレーションによる評価・・Ｎｉｎｆシステムの広域分散環境でのジョブスケジューリング実現にむけて", 並列処理シンポジウム JSPP’98,pp127-134, Ｊｕｎｅ 1998

[196] Satoshi Matsuoka, Hidetaka Ogawa, K Shimura, Yasunori Kimura, K. Hotta, Hiromishu Takagi "OpenJIT ---A Reflective Java JIT Compiler", Proc. OOPSLA 98 Workshop on Reflective Programming in C++ and Java,pp. 16--20, Oct. 1998.電子情報通信学会技術研究報告 CPSY98-67 (SWoPP 長岡 98)pp. 49 --56 August 1998

[197] Hidemoto Nakada, Atsuko Takefusa, Hirotaka Ogawa, Kento Aida, HiromitsuTakagi, Satoshi Matsuoka,Umpei Nagashima, Mitsuhisa Sato, Satoshi Sekiguchi "Ninf Global Computing System - Architecture, Features, and Performance", HPCN Workshop on Distributed Computing, 1998.

[198] Yuji Ayatsuka, Satoshi Matsuoka, Jun Rekimoto "Popup Vernier: A Tool for Sub-Pixel-Pitch Dragging with a Smooth Mode Transition", Proceedings of ACM Symposium on User Interface Software and Technology (UIST98)}, San Francisco, CA, pp. 39−48,The ACM Press, 1998

[199] Yuji Ayatsuka, Satoshi Matsuoka, Jun Rekimoto "Layered Penumbrae: a Effective 3{D} Feedback Technique", Proc. Asia Pacific Computer Human Interaction 1998 (APCHI98)}, 1998, pp.202 --209.

[200] Sachiko Kawachiya, Takeo Igarashi, Satoshi Matsuoka, Hidehiko Tanaka "Reduction of Overhead in Drawing Figures with Computer - Detailed Analyses of Drawing Tasks", Proc. Asia Pacific Computer Human Interaction 1998 (APCHI98), 1998, pp.11 -18

[201] Takeo Igarashi, Satoshi Matsuoka, Sachiko Kawachiya, Hidehiko Tanaka "A Drawing System for Rapid Geometric Design", CHI98 Summary (ACM Conference on Human Factors in Computing Systems)Los Angels, U.S.A, 18-23 April, 1998, pp.24-25.

[202] Kento Aida, Atsuko Takefusa, Hidemoto Nakada, Satoshi Matsuoka, Umpei Nagashima "A Performance Evaluation Model for Effective Job Scheduling in Global Computing Systems", 7th IEEE International Symposium on High Performance Distributed Computing (HPDC7)}, Chicago, 1998.

[203] Hiromitsu Takagi, Satoshi Matsuoka, Hidemoto Nakada, Satoshi Sekiguchi, Mitsuhisa Sato, Umpei Nagashima "Ninflet: a Migratable Parallel Objects Framework using Java ", ACM 1998 Workshop on Java for High-Performance Network Computing (JavaGrande98), pp.151 -159, 1998.

[204] Shin Takahashi, Satoshi Matsuoka, Ken Miyashita, Hiroshi Hosobe, Tomihisa Kamada "A Constraint-Based Approach for Visualization and Animation", Constraints: An International Journal, Vol. 3-1, 61-86, 1998.

[205] 河内谷 幸子, 五十嵐 健夫, 松岡 聡, 田中 英彦 "認知的負荷の少ないインタラクティブ描画方式の提案", コンピュータソフトウェア, Vol. 15-4, pp.16-26, 1998.

[206] 五十嵐 健夫, 松岡 聡, 河内谷 幸子, 田中 英彦 "対話的整形による幾何学的図形の高速描画", 情報処理学会論文誌, Vol.39-5, pp.1373-1384, 1998.

[207] 小川 宏高, 松岡 聡 "部分計算を用いた MPI プログラム最適化システム OMPI", 情報処理学会論文誌 Vol.39-6, pp. 1700--1708, 1998.

[208] 中田 秀基 高木 浩光, 松岡 聡, 長嶋 雲兵, 佐藤 三久, 関口 智嗣 "Ninf による広域分散並列計算", 情報処理学会論文誌 Vol. 39-6, pp. 1818--1826, 1998.

[209] 竹房 あつ子, 小川 宏高、松岡 聡、中田秀基, 高木 浩光, 佐藤 三久, 関口 智嗣, 長嶋 雲兵 "複数クライアントによる LAN/WAN での Ninf の性能", 情報処理学会論文誌, Vol. 39-6, pp.1827--1838, 1998.

[210] Hidemoto Nakada, Hiromitsu Takagi, Satoshi Matsuoka, Umpei Nagashima, Mitsuhisa Sato, Satoshi Sekiguchi "Utilizing the Metaserver Architecture in the Ninf Global Computing System", High-Performance Computing and Networking (HPCN) 98, LNCS 1401, pp.607 -616, 1998.

[211] Yutaka Ishikawa, Satoshi Sekiguchi, Hidemoto Nakada, Satoshi Matsuoka, Umpei Nagashima "Ninf and PM: Communication Libraries for Global Computing and High-performance Cluster Computing", Future Generation Computing Systems, vol. 13 (1997/98), No. 4-5, March 1998. pp. 349-359.

[212] 竹房 あつ子, 小川 宏高, 松岡 聡, 中田 秀基,佐藤 三久, 関口 智嗣, 長嶋 雲兵 "マルチクライアントによるネットワーク数値情報システム Ninf の性能", 並列処理シンポジウム JSPP’97 論文集,pp237-280,May 1997

[213] Satoshi Matsuoka, Hirotaka Ogawa, Atsuko Takefusa, Hidemoto Nakada, Kento Aida, Umpei Nagashima, Mitsuhisa Sato, Satoshi Sekiguchi "Preliminary Evaluation of Scheduling in Ninf: a Global Computing System", International Workshop on Innovative Architectures 97}, Maui, Hawaii, 1997.

[214] Satoshi Matsuoka, A. Nikami, Hirotaka Ogawa, Yutaka Ishikawa "Towards a Parallel C++ Programming Language based on Commodity Object-Oriented Technologies", Proceedings of International Scientific

Computing in Object-Oriented Parallel Environments Conference (ISCOPE97)}, Springer LNCS 1343, Mari na del Rey, California, 1997.

[215] Atsuko Takefusa, Satoshi Matsuoka,Hirotaka Ogawa, Hidemoto Nakada, Hiromitsu Takagi, Mitsuhisa Sato, Satoshi Sekiguchi, Umpei Nagashima "Multi-client LAN/WAN Performance Analysis of Ninf: a High-Performance Global Computing System", Proceedings of IEEE Supercomputing 97}, San Jose, CA,1997 ({¥em CD-ROM Procedings

[216] Takeo Igarashi, Satoshi Matsuoka, Sachiko Kawachiya, Hidehiko Tanaka "Interactive Beautification: A Technique for Rapid Geometric Design", Proceedings of ACM Symposium on User Interface Software and Technology (UIST97)}, Banff, Canada, pp.105 --114 1997

[217] Naphito Sato, Satoshi Matsuoka, Jean-Marc Jezequel, Akinori Yonezawa "A Methodology for Specifying Data Distribution using only Standard Object-Oriented Features", Proceedings of ACM/IEEE International Conference on Supercomputing (ICS97), Vienna, Austria , pp. 116-123, 1997.

[218] Masashi Toyoda, Buntarou Shizuki,Shin Takahashi, Satoshi Matsuoka, Etsuya Shibayama "Supporting Design Patterns in a Visual Parallel Data-flow Programming Environment", Proceedings of IEEE Symposium on Visual Languages (VL97)}, Sept. 23 -26, 1997, Capri, Italy, IEEE Society Press,1997

[219] Takeo Igarashi , Sachiko Kawachiya ,Satoshi Matsuoka, Hidehiko Tanaka "In Search for an Ideal Computer-Assisted Drawing System", Proceedings of IFIP Interact97, Sydney, Austria, pp. 104 -111, 1997.

[220] Mitsuhisa Sato, Hidemoto Nakada, Satoshi Sekiguchi, Satosi Matsuoka, Umpei Nagashima, Hiromitsu Takagi "Ninf: A Network based Information Library for a Global World-Wide Computing Infrastracture", Proc. HPCN97 (LNCS -1225), Vienna, Austria, pp. 491-502, 1997.

[221] 八杉 昌宏, 松岡 聡, 米澤 明憲 "スレッドベース実行における積極的データ転送のための Plan-Do 型コンパイル技法とその評価", 情報処理学会論文誌 Vol.38-9, 1997, pp.1840--1848.

[222] 八杉 昌宏, 松岡 聡, 米澤 明憲 "ABCL/EM-4:データ駆動並列計算機上の並列オブジェクト指向言語処理系の実装と評価", 情報処理学会論文誌, Vol. 38-9, 1997, pp.1790--1799.

[223] Hirotaka Ogawa, Satoshi Matsuoka "Ninf : World-Wide Computing 指向のネットワーク数値情報ライブラリ", インターネットコンファレンス96, 1996, pp. 73 -80.

[224] Sachiko Kawachiya, Takeo Igarashi, Satoshi Matsuoka, Hidehiko Tanaka "GIGA:A Pen-Based Constraint Drawing System,", Proc. OZCHI96(6th Australian Conference on Computer -Human Interaction),1996, pp.314-315.

[225] Hiroshi Hosobe, Satoshi Matsuoka, Akinori Yonezawa "Generalized Local Propagation:A Framework for Solving Constraint Hierarchies ", Proceedings of Symposium on Principles and Practice of Constraint Programming (CP96)}, Springer LNCS 1118, pp. 237 -251, 1996.

[226] Yuji Ayatsuka, Satoshi Matsuoka, Jun Rekimoto "Penumbrae for 3-D Interation", Proceedings of ACM Symposium on User Interface Software and Technology (UIST96), Seattle, Washington, ACM Press, 1996.

[227] Hirotaka Ogawa, Satoshi Matsuoka "OMPI: Optimizing MPI programs using Partial Evaluation", Proc. IEEE/ACM Supercomputing96, Pittsburgh, PA,IEEE Society Press, 1996 {¥em (proceedings in CD-ROM)}

[228] Satodhi Sekiguchi, Mitsuhisa Sato, Hidemoto Nakada, Satoshi Matsuoka, Umpei Nagashima "Ninf --- Network based Information Library for Globally High Performance Computing", Proc. Parallel Object-Oriented Methods and Applications}, Santa Fe, pp. 39-48, 1996

[229] Hidehiko Masuhara, Satoshi Matsuoka, Akira Yonezawa "Implementing Parallel Language Constructs Using a Reflective Object-Oriented Language", Proceedings of Reflection96, San Francisco, pp. 212 --233,1996

[230] Naohito Sato, Satoshi Matsuoka, Akinori Yonezawa "Hierarchical Collections: An Efficient Scheme to Build an Object-Oriented Distributed Class Library for Massively Parallel Computation", Proceedings of International Symposium on Object Technologies for Advanced Software (ISOTAS96)}, Kanazawa, Japan,S pringer LNCS 1049, pp.96-117, 1996

[231] Satoshi Matsuoka, Masahiro Yasugi, Kenjiro Taura, Tomihisa Kamada, Akinori Yonezawa "Compiling Concurrent Objects for MPPs", Parallel Language and Compiler Research in Japan, Bic, Nicolau and Sato (eds.), Kluwer Academic Press, pp.91--125,1996

[232] 増原 英彦, 松岡 聡, 米澤 明憲 "並列自己反映言語システムの部分計算によるコンパイル技法", 情報処理学会論文誌 (1996 年度論文賞受賞), vol 37-7, pp.1290--1298, 1996.

[233] Kenichi Asai, Satoshi Matsuoka, Akinori Yonezawa "Duplication and Partial Evaluation --- For a Better Understanding of Reflective Languages", Journal of Lisp and Symbolic Computation, 1996.

[234] Shin Takahashi, Satoshi Matsuoka, et al. "A Constraint-based Approach for Visualization and Animation", Proceedings of the International Workshop on Constraints for Graphics and Visualization (CGV95)}, 1995, pp.

103--117
[235] Hidehiko Masuhara, Saatoshi Matsuoka, Akinori Yonezawa "Compiling Away the Meta-Level in Object-Oriented Concurrent Reflective Languages Using Partial Evaluation", Proceedings of ACM SIGPLAN Conference on Object-Oriented Programming Systems, Languages, and Applications (OOPSLA95), SIGPLAN Notices, Vol.30, No. 10, pp.300-315, 1995.
[236] Yuuji Igarashi, Satoshi Matsuoka, Toshiyuki Masui "Adaptive Recognition of Implicit Structures in Human-Organized Layouts", Proceedings of IEEE Symposium on Visual Languages,Darmstat, Germany, pp. 258--266, 1995.
[237] Ken Miyashita, Satoshi Matsuoka, Shin Takahashi, Akinori Yonezawa "Interactive Generation of Graphical User Interfaces by Multiple Visual Examples", Proceedings of ACM Symposium on User Interface Software and Technology (UIST94), Los Angeles, pp.85 -94.
[238] Kenjiro Taura, Satoshi Matsuoka, Akinori Yonezawa "ABCL/f: A Future-Based Polymorphic Typed Concurrent Object-Oriented Language – Its Design and Implementation", Proceedings of DIMACS Workshop on Specification of Parallel World Scientific, pp.254-263, 1994.Algorithms, American Math. Society, pp.275-292, 1994.
[239] Konno, Nagatsuka, Kobayashi, Matsuoka, Yonezawa "PARCS: An MPP-Oriented CLP Language", Proceedings of International Symposium on Parallel Symbolic Computation (PASCO94)}World Scientific, pp.254 -263, 1994.
[240] Shin Takahashi, Ken Miyashita, Satoshi Matsuoka, Akinori Yonezawa "A Framework for Constructing Animations via Declarative Mapping Rules", Proceedings of IEEE Symposium on Visual Languages,St. Louis, pp.314-322, 1994.
[241] Tomihisa Kamada, Satoshi Matsuoka, Akinori Yonezawa "Efficient Parallel Global Garbage Collection on Massively Parallel Computers", Proceedings of IEEE/ACM Supercomputing Washington D.C. , pp79-88, 1994.
[242] Masahiro Yasugi, Satoshi Matsuoka, Akinori Yonezawa "The Plan-Do Style Compilation Technique for Eager Data Transfer in Thread-Based Exection", Proceedings of IFIP WG10.3 Intl. Conf. onParallel Architectures and Compiler Technology (PACT) 94}, Montreal, Canada, pp.57 --66, Aug. 1994.
[243] Hiroshi Hosobe, Ken Miyashita, Shin Takahashi, Satoshi Matsuoka, Akinori Yonezawa "Locally Simultaneous Constraint Satisfaction", Proceedings of the Second Workshop on Principles and Practices of Constraint Programming}, Orcas Island, Washington,Springer LNCS 874, pp.51--62, May 1994.
[244] 高橋 伸, 松岡 聡, 宮下 健, 米澤 明憲 "Constructing Algorithm Animations via Declarative Specifications", コンピュータソフトウェア, 11(6), 1994, pp. 83--94.
[245] 宮下 健, 松岡 聡, 高橋 伸, 米澤 明憲 "Interactive Generation of Graphical User Interfaces by Multiple Visual Examples", コンピュータソフトウェア, 11(6), 1994, pp. 41--51.
[246] 一杉 裕志, 松岡 聡, 米澤 明憲 "The Implementation of a Reflective Object-Oriented Concurrent Language with out a Run-time Kernel", コンピュータソフトウェア, 11(3), 1994, pp. 65--77.
[247] 増原 英彦, 松岡 聡, 米澤 明憲 "The Design and Implementation of an Object-Oriented Concurrent Reflective Language ABCL/R2", コンピュータソフトウェア, 11(3), 1994, pp. 15--32.
[248] Kenjiro Taura, Satoshi Matsuoka, Akinori Yonezawa in Robert H. Halstead, Jr., and Takayasu Ito (Eds.), "An Efficient Implementation Scheme of Concurrent Object-Oriented Languages on Stock Multicomputers", Parallel Symbolic Computing: Languages, Systems, and Applications (US/Japan Workshop Proceedings) Springer-Verlag Lecture Notes in Computer Science 748,Nov. 1993.
[249] Satoshi Matsuoka, Kenjiro Taura, Akinori Yonezawa "Highly Efficient and Encapsulated Re-use of Synchronization Code in Concurrent Object-Oriented Languages", Proceedings of ACM OOPSLA93, Washington D.C., Sep. 1993.
[250] Kenjiro Taura, Satoshi Matsuoka, Akinori Yonezawa "An Efficient Implementation Scheme of Concurrent Object-Oriented Languages on Stock Multicomputers", Proceedings of 4th ACM Symposium on Principles and Practices of Parallel Programming (PPoPP93)}, San Di ego, CA, May 20-22, 1993.
[251] Kenichi Asai, Satoshi Matsuoka, Akinori Yonezawa "Model Checking of Control-Finite CSP Programs", Proceedings of 26th Hawaii International Conference on System Sciences Jan. 1993.
[252] Satoshi Matsuoka, Akinori Yonezawa In G. Agha et. al. eds. "Analysis of Inheritance Anomaly in Object-Oriented Concurrent Programming Languages", Research Directions in Concurrent Object-Oriented Programming,The MIT Press, 1993.
[253] Akinori Yonezawa, Satoshi Matsuoka, Masahiro Yasugi, Kenjiro Taura "Efficient Implementation of Concurrent Object-Oriented Languages on Multicomputers", IEEE Parallel and Distributed Technology}, Vol. 1, No. 2, 1993.
[254] Ken Miyashita, Satoshi Matsuoka, Shin Takahashi, Akinori Yonezawa, Tomihisa Kamada "Declarative

Programming of Graphical Interfaces by Visual Examples", Proceedings of ACM User Interface Software and Technology92 (UIST92)}, Monterey, CA, Nov. pp. 15 -18, 1992.

[255] Yuuji Ichisugi, Satoshi Matsuoka, Akinori Yonezawa "RbCl: A Reflective Concurrent Language without a Run-Time Kernel", Proceedings of IMSA92 International Workshop on Ref lection and Metalevel Architectures}, Tokyo, Japan, Nov. 4-7, 1992.

[256] Hidehiko Masuhara, Satoshi Matsuoka, Takuo Watanabe, Akinori Yonezawa, "Object-Oriented Concurrent Reflective Languages can be Implemented Efficiently", Proceedings of ACM OOPSLA92, Vanc ouver, Canada, Oct. 1992.

[257] Toshio Tonouchi, Ken Nakayama, Satoshi Matsuoka, Satoru Kawai "Creating Visual Objects by Direct Manipulation", Proceedings of IEEE International Workshop on Visual Languages,Seattle, Washington, Sept. 1992.

[258] Masahiro Yasugi, Satoshi Matsuoka, Akinori Yonezawa "ABCL/onEM-4: A New Software/Hardware Architecture for Object-Oriented Concurrent Computing on an Extended Dataflow Supercomputer", Proceedings of ACM Int. Conf. on Supercomputing}, Washington D.C.,July 19-23, 1992

[259] Naoki Kobayashi, Satoshi Matsuoka, Akinori Yonezawa "Control in Parallel Constraint Logic Programming", Proceeding of Logic Programming Conference 91,Springer Lecture Notes in Artificial Intelligence, 1992.

[260] Shin Takahashi, Satoshi Matsuoka, Akinori Yonezawa, Tomihisa Kamada "A General Framwork for Bi-Directional Translation between Abstract and Pictorial Data ", Proceedings of the ACM User Interface Software and Technology91 (UIST91), South Carolina, Nov. 1 1-13, 1991, pp.165-174.

[261] Satoshi Matsuoka, Takuo Watanabe, Yuuji Ichisugi, Akinori Yonezawa "Object-Oriented Concurrent Reflective Architectures", Proceedings of ECOOP Workshop on Object-Based Concurrent Programming,Geneve, Switzerland, July, 1991, Springer Lecture Notesin Computer Science, Vol.612, pp.211-226, 1992.

[262] Satoshi Matsuoka, Shin Takahashi, Tomihisa Kamada, Akinori Yonezawa "A General Framwork for Bi-Directional Translation between Abstract and Pictorial Data", ACM Transactions on Information Systems}, Vol. 10, No. 4,Oct. 1992, pp. 408-437.

[263] Satoshi Matsuoka, Takuo Watanabe, Akinori Yonezawa "Hybrid Group Reflective Architecture for Object-Oriented Concurrent Reflective Programming ", Proceedings of the European Conference on Object-Oriented Programming (ECOOP91)}, Springer Lecture Notes in C omputer Science, Vol.512, 1991, pp.231-250.

[264] Satoshi matsuoka, Shinichi Furuso, Akinori Yonezawa "A Fast Parallel Conservative Garbage Collector for Concurrent Object-Oriented Systems", Proceedings of IEEE International Workshop on Object Orientation in Operating Systems (I-WOOOS91)}, Palo Alto, Oc t, 1991, pp.87-93.

[265] Yuuji Ichisugi, Satoshi Matsuoka, Takuo Watanabe, Akinori Yonezawa "An Object-Oriented Concurrent Reflective Architecture for Distributed Computing Environments", Proceedings of 29th Annual Allerton Conference on Communication, Control and Computing, Allerton Illinois, 1991.

[266] Ken Nakayama, Satoshi Matsuoka, Satoru Kawai "Visualization of Abstract Concepts Using Generalized Path Binding", Proceedings of the CG International90,pp.377 -401, Springer-Verlag, 1990.

[267] Satoshi Matsuoka,Tomihisa Kamada, Satoru Kawai "Asympototic Evaluation of Window Visibility", Information Processing Letters, 31(3):119-126, May 1989.

[268] Satoshi Matsuoka, Satoru Kawai "Using Tuple Space Communication in Distributed Object-Oriented Languages", Proceedings of Object-Oriented Programming Systems, Languages and Applications (ACM OOPSLA88)}, pp.276-284, 1988.